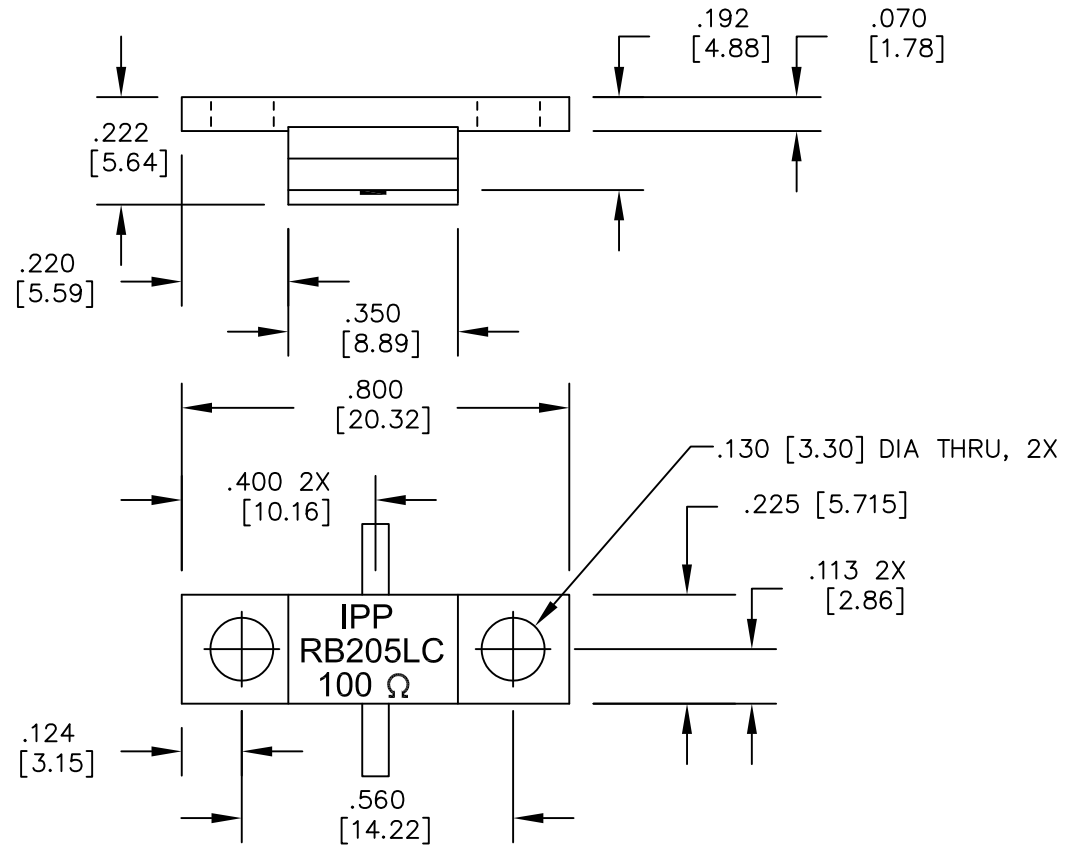


UNAUTHORIZED USE OF THIS  
DRAWING IS STRICTLY PROHIBITED

DAR	REV	DESCRIPTION	APPVD	DATE
1484	A	INITIAL RELEASE	DG	9-08
1562	B	CHANGE DIMS TO MULT PLACES	DG	9-08
1563	C	CHANGE DIM FROM .229 TO .225	DG	10-08



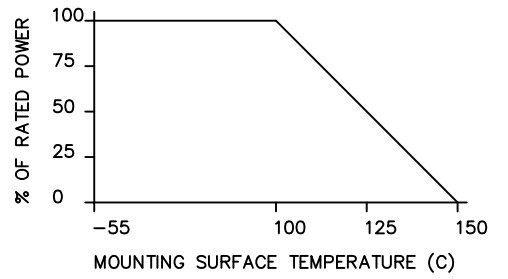
**NOTES**

- LEAD MATERIAL:  
99.99% PURE SILVER,  
.050 WIDE x .005 THICK
- LEAD LENGTH: .150 MIN.
- SUBSTRATE MATERIAL: BeO  
COVER MATERIAL: Al<sub>2</sub>O<sub>3</sub>
- FLANGE MATERIAL: COPPER  
PLATING: NICKEL
- DIMENSIONS: INCHES, [mm]

ELECTRICAL SPECIFICATIONS

INPUT POWER: 60 WATT  
 FREQUENCY: DC - 4.0 GHz.  
 CAPACITANCE: 0.75 pF

POWER DERATING CURVE



(REF: 100 OHM VALUE SHOWN)

PART NO.	VALUE	TOLERANCE
IPP-RB205LC-50	50 OHMS	+/- 5%
IPP-RB205LC-100	100 OHMS	+/- 5%

MATERIAL [3] [4]	DWN S.Cornell	DATE 7-17-08		
	ENG TJD	DATE 9-18-08		
FINISH [4]	MANUF RR	DATE 9-18-08		TITLE RESISTOR, LOW C, 60 WATT, FLANGE
	CAGE CODE: 437R2		P/N IPP-RB205LC-XXX	
TOLERANCE IN [MM]	.XX ±.02 [.50] .XXX ±.010 [.254]	SHEET OF 1 1		SCALE 4:1